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ПвЭгаП-110 1x185 ТУ У 31.3-00214534-060:2011

Power cables with copper conductor, with XLPE, longitudinal and transverse screen sealing and polyethylene outer sheath

For the cable of this mark correspond the foreign-made analogues:

N2XS(FL)2Y (DE) • 2XS(FL)2Y (DE) • HXCHBMK (FI) • Cu/XLPE/CWS/LW/MDPE (GB) • XRUHKXS (PL) • Π B Π 2Γ (RU)

Technical cable requirements correspond to IEC 60840

Cables are used for laying:

- · in soil (trenches)
- · in damp, partially flooded premises
- · in ground with high humidity
- in non-navigable waters
- in the air, including cable structures, if provided the additional fire protection

It is possible to manufacture cables with extruded semiconductor layer along outer sheath.

Order entry example:

ПвЭгаП-П-110 1х185/95 ТУ У 31.3-00214534-060:2011

An extruded semiconductor layer along outer sheath ensures the correct testing of cable line with sections of underground laying in polymer pipes.

It is possible to manufacture cables with an integrated fiber-optic module.

Order entry example:

ПвЭгаП-110 1х185/95 (ОМ) ТУ У 31.3-00214534-060:2011

In conjunction with the DTS system, the integrated fiber-optic module can act as a distributed cable line temperature sensor.

It is possible to manufacture cable with sealed conductor.

Order entry example:

ПвЭгаП-110 1х185/95 (г) ТУ У 31.3-00214534-060:2011







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TECHNICAL SPECIFICATIONS

Rated voltage	kV	110
Maximum voltage	kV	126
Conductor rated area	mm²	185
Minimum screen cross-section	mm²	35
Partial discharge factor for rated voltage, not more than	рС	6
Permissible short circuit current across the screen of	kA	7.1
minimum cross-section		
Maximum permissible short-circuit current in core	kA	26.5
Permissible continious current rating by aerial laying *		
in trefoil formation with double-side screen earthing	Α	532
in trefoil formation with single-side screen earthing or	Α	545
cross screen earthing		
plane with double-side screen earthing	Α	573
• plane with single-side screen earthing or cross screen	Α	624
earthing		
Permissible continious current rating by burial *		
in trefoil formation with double-side screen earthing	Α	435
in trefoil formation with single-side screen earthing or	Α	449
cross screen earthing		
plane with double-side screen earthing	Α	422
plane with single-side screen earthing or cross screen	Α	468
earthing		
Maximum permissible conductor temperature		
Continious	°C	+90
in emergency operation	°C	+130
• at short circuit	°C	+250
Operating temperature range	°C	-60 +50
Minimum bending radius by laying	mm	1072
Rated outer diameter of the cable (for reference) **	mm	67
Cable weight (approximate)	kg/km	5660
Rated factory cable length and gross weight of the delivery	m, t	# 22УД-60: 339 • 2.8
on the drums ***		# 25УД-90: 611 • 5.0
		# 26УД-100: 869 • 6.7

Notes:

When ordering it is neccesary to agree the factory length of the product with the manufacturer

^{*} Long permissible current loads are calculated for the following conditions: conductor temperature 90 °C, air temperature 30 °C, soil temperature 20 °C, load factor 1.0, thermal resistivity of soil 1.0 °K•m/W, laying depth in the ground 1.5 m, while laying in flat formation the distance between cables in clear is equal to the cable diameter, while laying in trefoil formation cables are laid side by side

^{**} The external diameter may differ from the rated up to \pm 10 %

^{***} Отклонение фактической массы брутто от указанного значения может составлять ± 7 %



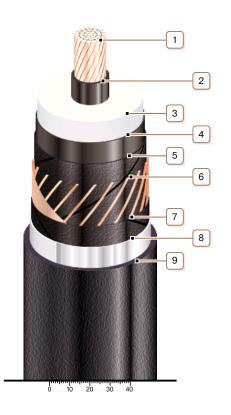




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CONSTRUCTION

1. Copper multiwire compact conductor

Note: It is possible to manufacture cable with sealed conductor.

- 2. Inner extruded semiconducting layer
- 3. XLPE insulation
- 4. Outer extruded semiconducting layer
- 5. Lapping layer of semiconductive swellable tape
- 6. Copper screen

Note: It is possible to manufacture a cable with a fiber optic module built into the screen, including as a DTS system sensor

- 7. Lapping layer of semiconductive swellable tape
- 8. Alumopolymer tape
- 9. Outer sheath of polyethylene or polyethylene copolymer

Note: It is possible to manufacture cable with extruded semiconductor layer along outer sheath