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## ПвЭСПу-10 1х1400 ТУ У 27.3-00214534-092:2016

Single-core power cables with copper conductors, XLPE-insulated, with copper screen, lead-sheathed, with reinforced outer sheath of polyethylene

Technical cable requirements correspond to IEC 60502-2

Cables are used for laying:

- in places, where small mechanical impacts on cable are possible, including tensile forces
- · in soil (trenches) with high corrosiveness
- · in damp, partially flooded premises
- · in wetlands
- · in non-navigable waters
- · on difficult route sections, according to the unique specification
- in the air, including cable structures, if provided the additional fire protection

It is possible to manufacture cables with extruded semiconductor layer along outer sheath.

Order entry example:

ПвЭСПу-П-10 1х1400/95 ТУ У 27.3-00214534-092:2016

An extruded semiconductor layer along outer sheath ensures the correct testing of cable line with sections of underground laying in polymer pipes.

It is possible to manufacture cables with an integrated fiber-optic module.

Order entry example:

ПвЭСПу-10 1х1400/95 (ОМ) ТУ У 27.3-00214534-092:2016

In conjunction with the DTS system, the integrated fiber-optic module can act as a distributed cable line temperature sensor.

Cables without copper screen can also be manufactured

It is possible to manufacture cable with sealed conductor.

Order entry example:

ПвЭСПу-10 1х1400/95 (г) ТУ У 27.3-00214534-092:2016







# ПвЭСПу-10 1х1400 ТУ У 27.3-00214534-092:2016



Single-core power cables with copper conductors, XLPE-insulated, with copper screen, lead-sheathed, with reinforced outer sheath of polyethylene

## **TECHNICAL SPECIFICATIONS**

Rated voltage	kV	10
Maximum voltage	kV	12
Number and rated area of conductors	mm²	1 x 1400
Insulation thikness	mm	3.4
Sheath thikness	mm	2.7
Maximum permissible short-circuit current in core	kA	200
Permissible continious current rating *		
by aerial laying in trefoil formation	Α	1620
by aerial flat laying	Α	2234
by burial in trefoil formation	Α	1039
• by burial flat	Α	880
Partial discharge factor for rated voltage, not more than	рС	6
Maximum permissible conductor temperature		
Continious	°C	+90
in emergency operation	°C	+130
at short circuit	°C	+250
Operating temperature range	°C	-60 +50
Minimum bending radius by laying	mm	2075
Rated outer diameter of the cable (for reference) **	mm	83
Cable weight (approximate)	kg/km	24380
Rated factory cable length and gross weight of the delivery	m, t	# 30УД-130: **** 293 • 10.0
on the drums ***		

#### Notes:

When ordering it is neccesary to agree the factory length of the product with the manufacturer

<sup>\*</sup> Long permissible current loads are calculated for the following conditions: conductor temperature 90 °C, air temperature 30 °C, soil temperature 20 °C, thermal resistivity of soil 1.5 °K·m/W, laying depth in the soil 0.8 m, while laying in flat formation the distance between cables in clear is equal to the cable diameter, while laying in trefoil formation cables are laid side by side, shields are earthed on both ends of the line

<sup>\*\*</sup> The external diameter may differ from the rated up to  $\pm$  10 %

<sup>\*\*\*</sup> Отклонение фактической массы брутто от указанного значения может составлять  $\pm\,7\,\%$ 

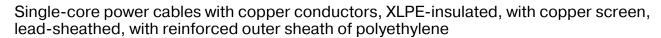
<sup>\*\*\*\*</sup> Option delivery on not full drum

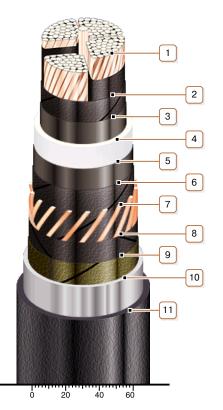






# ПвЭСПу-10 1x1400 ТУ У 27.3-00214534-092:2016





### CONSTRUCTION

- 1. Copper segmentary multiwire compact conductor
- It is possible to manufacture cable with sealed conductor.
- · Conductor segment twisting is not illustrated
- 2. Lapping layer of semiconductive swellable tape
- 3. Inner extruded semiconducting layer
- 4. XLPE insulation
- 5. Outer extruded semiconducting layer
- 6. Lapping layer of semiconductive swellable tape
- 7. Copper screen

Note: Cables without copper screen can also be manufactured

- 8. Lapping layer of semiconductive swellable tape
- 9. Lapping layer of semiconductive tape
- 10. Lead sheath
- 11. Strengthened polyethylene outer sheath

Note: It is possible to manufacture cable with extruded semiconductor layer along outer sheath