



АПвЭгаАкП-10 1x800 **ТУ У 31.3-00214534-017-2003**

Single-core power cables with aluminium conductor, with XLPE, aluminium-wire armoured, with longitudinal and transverse screen sealing and polyethylene outer sheath

Due to non-magnetic armour, cables operate at AC
Technical cable requirements correspond to IEC 60502-2

Cables are used for laying:

- *in places, where small mechanical impacts on cable are possible, including tensile forces*
- *in soil (trenches)*
- *in damp, partially flooded premises*
- *in ground with high humidity*
- *in non-navigable waters*
- *in the air, including cable structures, if provided the additional fire protection*

It is possible to manufacture cables with extruded semiconductor layer along outer sheath.

Order entry example:

АПвЭгаАкП-П-10 1x800/35 ТУ У 31.3-00214534-017-2003

An extruded semiconductor layer along outer sheath ensures the correct testing of cable line with sections of underground laying in polymer pipes.

It is possible to manufacture cables with an integrated fiber-optic module.

Order entry example:

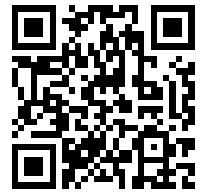
АПвЭгаАкП-10 1x800/35 (ОМ) ТУ У 31.3-00214534-017-2003

In conjunction with the DTS system, the integrated fiber-optic module can act as a distributed cable line temperature sensor.

It is possible to manufacture cable with sealed conductor.

Order entry example:

АПвЭгаАкП-10 1x800/35 (r) ТУ У 31.3-00214534-017-2003



АПвЭгаАкП-10 1x800 ТУ У 31.3-00214534-017-2003

Single-core power cables with aluminium conductor, with XLPE, aluminium-wire armoured, with longitudinal and transverse screen sealing and polyethylene outer sheath

TECHNICAL SPECIFICATIONS

Rated voltage	kV	10
Maximum voltage	kV	12
Number and rated area of conductors	mm ²	1 x 800
Insulation thickness	mm	3.4
Minimum screen cross-section	mm ²	35
Permissible short circuit current across the screen of minimum cross-section	kA	7.1
Maximum permissible short-circuit current in core	kA	75.2
Permissible continuous current rating *		
• by aerial laying in trefoil formation	A	1041
• by aerial flat laying	A	1132
• by burial in trefoil formation	A	664
• by burial flat	A	647
Partial discharge factor for rated voltage, not more than	pC	6
Maximum permissible conductor temperature		
• Continuous	°C	+90
• in emergency operation	°C	+130
• at short circuit	°C	+250
Operating temperature range	°C	-60 ... +50
Minimum bending radius by laying	mm	1024
Rated outer diameter of the cable (for reference) **	mm	64
Cable weight (approximate)	kg/km	5090
Rated factory cable length and gross weight of the delivery on the drums ***	m, t	# 22УД-60: 450 • 3.2 # 25УД-90: 771 • 5.5

Notes:

When ordering it is necessary to agree the factory length of the product with the manufacturer

* Long permissible current loads are calculated for the following conditions: conductor temperature 90 °C, air temperature 30 °C, soil temperature 20 °C, thermal resistivity of soil 1.5 °K·m/W, laying depth in the soil 0.8 m, while laying in flat formation the distance between cables in clear is equal to the cable diameter, while laying in trefoil formation cables are laid side by side, shields are earthed on both ends of the line

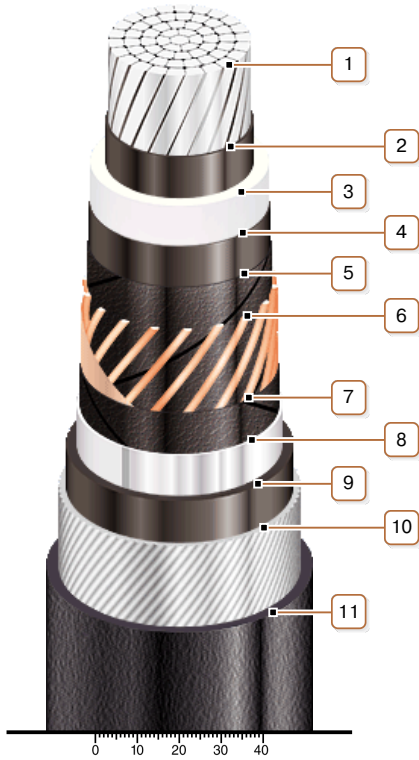
** The external diameter may differ from the rated up to ± 10 %

*** Отклонение фактической массы брутто от указанного значения может составлять ± 7 %



АПвЭгаАкП-10 1x800 ТУ У 31.3-00214534-017-2003

Single-core power cables with aluminium conductor, with XLPE, aluminium-wire armoured, with longitudinal and transverse screen sealing and polyethylene outer sheath



CONSTRUCTION

1. Aluminium multiwire compacted conductor

Note: It is possible to manufacture cable with sealed conductor.

2. Inner extruded semiconducting layer

3. XLPE insulation

4. Outer extruded semiconducting layer

5. Lapping layer of semiconductive swellable tape

6. Copper screen

7. Lapping layer of semiconductive swellable tape

8. Alumopolymer tape

9. Extruded bedding of polyethylene

10. Aluminium-wire armour

11. Outer sheath of polyethylene or polyethylene copolymer

Note: It is possible to manufacture cable with extruded semiconductor layer along outer sheath